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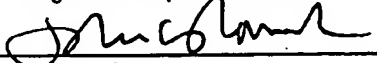
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December 13, 2007

Date of Deposit

John G. Rauch (37,218)

Name of applicant, assignee or
Registered Representative



Signature

12/13/2007
Date of Signature

Our Case No. 13050/12

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Shawn D. ROGERS et al.

Serial No. 10/802,664

Filing Date: 03/17/2004

For: CIRCUIT AND METHOD FOR
ENHANCED LOW FREQUENCY
SWITCHING NOISE SUPPRESSION IN
MULTILAYER PRINTED CIRCUIT
BOARDS USING A CHIP CAPACITOR
LATTICE

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)
) Examiner: Dickey, Thomas L.

)
) Art Unit: 2826

)
) Confirmation No.: 2665

**AMENDMENT AND RESPONSE TO OFFICE ACTION TO ACCOMPANY A REQUEST
FOR CONTINUING EXAMINATION (RCE)**

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office action dated June 18, 2007, and the advisory action of August 23, 2007 and accompanies a Request for Continuing Examination (RCE). The Claims begin on page 2, and the Remarks begin on page 14.